PRELIMINARY TECHNICAL INFORMATION

Adaptation board for 2ED300C17-S

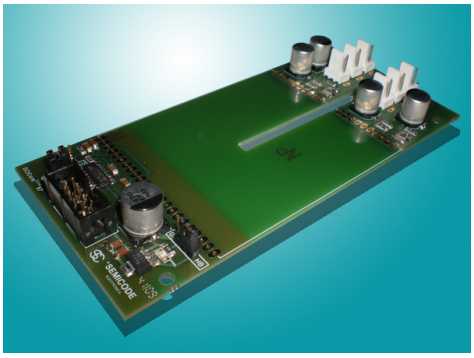


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DEFAULT VALUES (board configuration)
-Half Bridge mode (Direct Mode optional)
-4μS dead time between channels generated
-0-5V_input PWM and reset logic levels (0-15V optional)
-0-5V logic fault output (0-15V optional)
(low level-open collector output optional)

SCi*Core*Link**67** is a printed circuit board specially designed for the driver module **2ED300C17-S.** For more accuracy information about the working of the entire driver board, consult the datasheet of the driver module. Configuration other than the default one is available by order, in that case, please contact with us.

ELECTRICAL CHARACTERISTICS

Description	*symbol*	*notes/test conditions*	*Min*	*Typ*	*Max*	*Units*
Supply voltage	V_{CC}		14	15	16	V
PWM & reset High state Input voltage	V_{IN_H}	JP1 left connected	3,5		5	V
PWM & reset Low state Input voltage	V_{IN_L}		0		1,5	V
PWM & reset High state Input voltage	V_{IN_H}	JP1 right connected	11		15	V
PWM & reset Low state Input voltage	V_{IN_L}		0		4	V
Fault open collector current	I_{FAULT}	working as open collector			20	mA
Fault High state Output Voltage	V_{FAULT_H}	working in logic level; JP1 left connected			5	V
Fault High state Output Voltage	V_{FAULT_H}	working in logic level; JP1 right connected			15	V
Fault Low state Output Voltage	V_{FAULT_L}	**indicates fault condition** open collector or logic level	0			V

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CON 1	designation	function
1	PWM_{TOP}	Input logic signal for switching TOP IGBT
2	GND	Ground terminal for supply and logic signals
3	GND	Ground terminal for supply and logic signals
4	RESET	reset input signal (low state
5	GND	Ground terminal for supply and logic signals
6	VCC	+15VDC for supply voltage
7	FAULT	fault output signal
8	VCC	+15VDC for supply voltage
9	VCC	+15VDC for supply voltage
10	PWM_{BOT}	Input logic signal for switching BOT IGBT

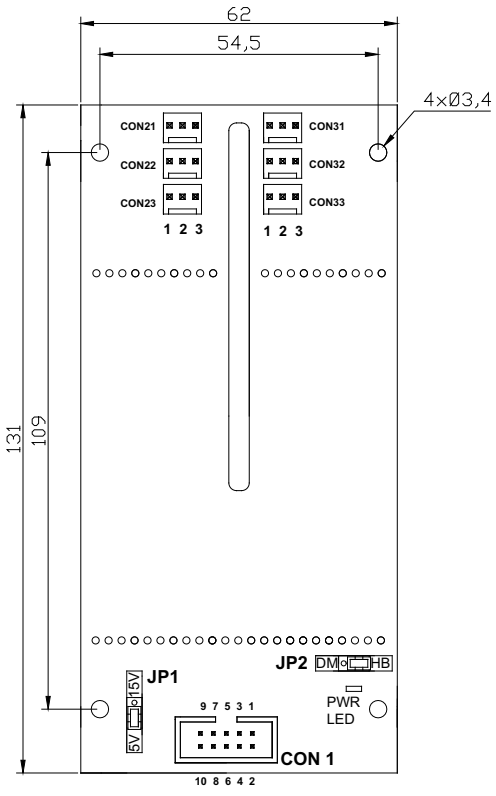
CON 2X	designation	function
1	$COLLECTOR_{TOP}$	TOP IGBT collector desaturation sensing
2	$GATE_{TOP}$	TOP IGBT gate signal
3	$EMITTER_{TOP}$	TOP IGBT emiter signal

CON 3X	designation	function
1	$COLLECTOR_{BOT}$	BOT IGBT collector desaturation sensing
2	$GATE_{BOT}$	BOT IGBT gate signal
3	$EMITTER_{BOT}$	BOT IGBT emiter signal

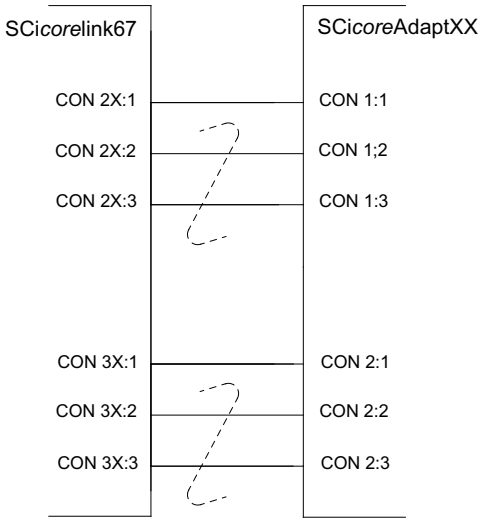
JUMPERS	
JP1	right connected: 15V logic level left connected: 5V logic level
JP2	right connected: half bridge mode left connected: direct mode

conector CON1:
type TYCO 1-1634688-0

conector CON2X, CON3X:
type MOLEX 22-27-2031



All dimensions are in mm



mounting considerations:
cables between pcboard and IGBT must be as short as possible. Gate and emitter conections slould be twisted pair

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