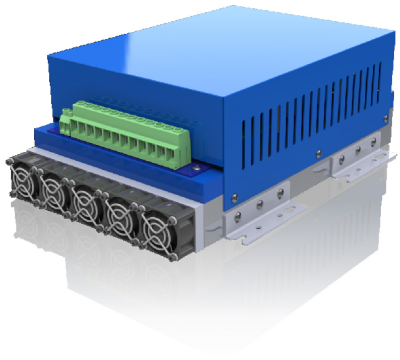
MTL-CBI0060F12IXHZ

MT Series

PRELIMINARY TECHNICAL INFORMATION

HIGHLIGHTS

- CBI topology
- Compact design
- Current, voltage and temperature sensors
- IGBT drivers included
- Ready to use



non-contractual photo

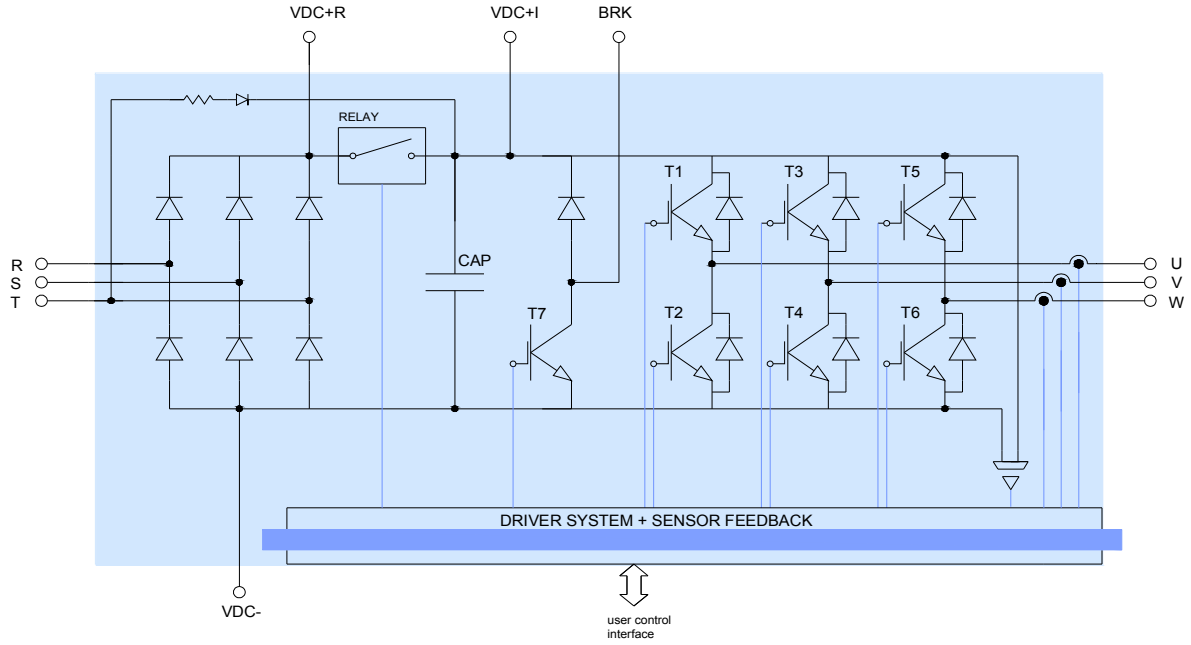
ABOUT MTL SERIES

RECTIFICADORES GUASCH S.A. offers a compact and ready-to-use Converter-Brake-Inverter power stack for motor control or inverter applications. This power stack includes the IGBTs (CBI module) with a heatsink, the optocoupled drivers, output phase current sensors, DC-Link voltage sensors and module temperature. The customer can work in a short delivery time with a wide range of power assemblies in a compact size.

The MTL series takes the low range of power from our MT Series of IGBTs power stacks. MT series is suitable to realize converters, choppers, half, full or three phase bridge inverters for motor control, welding, renewable energies, UPS, etc.

BRIEFING

Topology	B6U+ capacitor bank + brake + B6I	
Market	industrial	
Cooling system	axial fan	
Driver system	SCiCoreDrive72	
Parameters monitorized	DC-Link voltage, Output current (3 phase) internal module NTC	
Max Voltage applied to DClink		800 V
Output current per phase	f_{sw}=10 kHz, T_J<125ºC T_{env}=40ºC f_O=50 Hz, PF=0.85, m=1, V_{IN} =400 V_{RMS}	30 A



111122 Rev.:3

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POWER STACK GENERAL CHARACTERISTICS

Description	symbol	notes/test conditions	Min	Typ	Max	Units
Input AC voltage	$V_{IN\ RMS}$	network voltage (+15%)		400	460	V_{RMS}
Max DC voltage	V_{DClink}				800	V_{DC}
Output current per phase	$I_{U,V,W}$	fsw=10kHz, TJ<125ºC Tamb=40ºC fo=50Hz, PF=0,85, m=1, Vin rms=400V			30	A_{RMS}
IGBT maximum junction temperature	T_{Jmax}				150	ºC
IGBT temp. under switching conditions	$T_{J\ (sw)}$		-40		125	ºC
Storage temperature	T_{stg}		-40		85	ºC
Operating temperature	T_{op}		-25		85	ºC
Power-to-control isolation voltage	V_{ISOp-c}	50 Hz @1min/ **note 1**	3			kV_{AC}
Module isolation voltage	V_{ISOmod}	50 Hz @1min/ **note 2**	3			kV
Weight (aprox)				5.8		kg

COOLING SYSTEM CHARACTERISTICS

Description	symbol	notes/test conditions	Typical	Units
Type		forced		
Fan system supply voltage	V_{FAN}		12	V_{DC}
Fan system consumption	I_{FAN}		700	mA

RECTIFIER BRIDGE CHARACTERISTICS

Description	symbol	notes/test conditions	Min	Typ	Max	Units
Repetitive reverse voltage	V_{RRM}	T_J=25ºC			1600	V
Average forward current	I_{FAV}	T_C=80ºC, T_J=150ºC			70	A
Max DC output rectifier current	I_{FRMS}	T_C=80ºC, T_J=150ºC			190	A
Forward voltage	V_F	Ic=70 A, TJ=125ºC		1,12		V

CAPACITOR BANK CHARACTERISTICS

Description	symbol	Typical		Units
Single capacitor	C	Electrolytic type	820uF 400V	
Total equivalent capacitance	C_{equiv}	820		µF
max capacitor bank DC voltage	V_{DClink}	1000		V

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BRAKE ARM CHARACTERISTICS

Description	symbol	notes/test conditions	Min	Typ	Max	Units
BRAKE IGBT						
Collector-Emitter Voltage	V_{CES}	T_J=25ºC			1200	V
DC Collector current	I_{C80}	T_C=80ºC, T_J=150ºC			40	A
Collector-emitter saturation voltage	V_{CEsat}	Ic=40 A, TJ=125ºC		2.7		V
BRAKE DIODE						
Repetitive reverse voltage	V_{RRM}	T_J=25ºC			1200	V
Average forward current	I_{FAV}	T_C=80ºC, T_J=150ºC			29	A
Forward voltage	V_F	Ic=29 A, TJ=125ºC		1.98		V

INVERTER CHARACTERISTICS

Description	symbol	notes/test conditions	Min	Typ	Max	Units
INVERTER IGBT						
Collector-Emitter Voltage	V_{CES}	T_J=25ºC			1200	V
DC Collector current	I_{C80}	T_C=80ºC, T_J=150ºC			60	A
Collector-emitter saturation voltage	V_{CEsat}	Ic=60 A, TJ=125ºC		2.61		V
FREE WHEELING DIODE						
Repetitive reverse voltage	V_{RRM}	T_J=25ºC			1200	V
Average forward current	I_{FAV}	T_C=80ºC, T_J=150ºC			59	A
Forward voltage	V_F	Ic=59 A, TJ=125ºC		1.99		V

ENVIRONMENTAL SPECIFICATIONS

Description	
Protection grade (EN-60529 / CEI529 / UNE-20324)	IP-00
Humidity max.	50% RH @ 35ºC / 90% RH @ 20ºC
Pollution degree	III

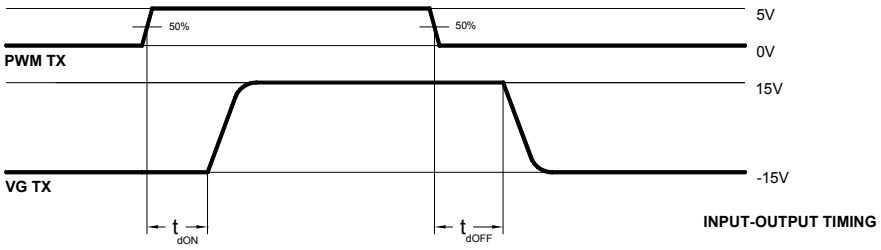
MOUNTING CONSIDERATIONS

It's necessary a minimum distance of 100 mm with regard to the envelope. The free air circulation should be guaranteed. Avoiding the heat sources of nearby to assembly.
In the real applications it is important to consider a safety margin with regarding the working current, we recommend a margin of the 20%.
For critical cases (24 hours work, repetitive overloads...), margins of the 30% to 50% are used.

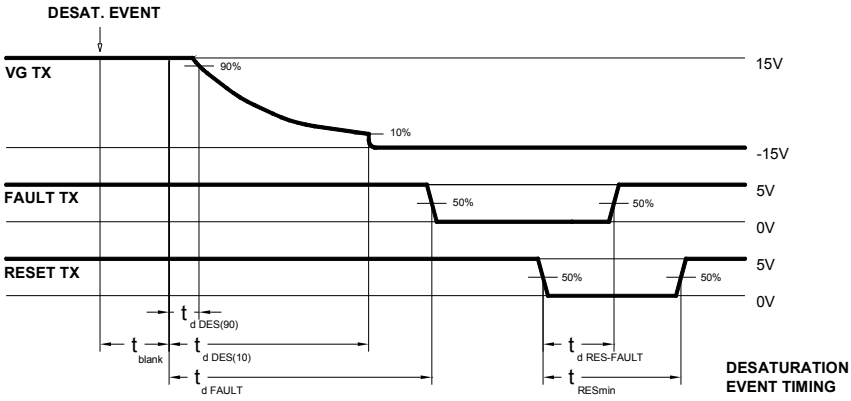
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DRIVERS TIMMING AND LOGIC LEVELS

Description	symbol	notes/test conditions	min.	typ.	max.	units
Logic low input voltages (PWM & reset)	$V_{IN,RESET}$		0.2		5	V
Logic high input voltages (PWM & reset)	$V_{IN,RESET}$		0		0.8	V
Logic high input voltages (relay)	V_{RELAY}			5		V
Fault output current	I_{FAULT}				8	mA
Logic low input current (PWM & reset)	$I_{IN,RESET}$		-0.5	-0.4		mA
Logic high input current (RELAY)	$I_{IN,RELAY}$	V_{RELAY} = 5V			20	mA
high output propagation time	$t_{d\ ON}$			300		ns
				440		
low output propagation time	$t_{d\ OFF}$			320		ns
		C_G=10nF		460		
desat. detection to FAULT output delay	$t_{d\ FAULT}$	R_G=15Ω		1.8	5	µs
blanking time	t_{blank}	f_{sw}=10kHz			2.8	µs
desat. detection to 10% turn off	$t_{d\ DES(10)}$			0.3	0.5	µs
desat. detection to 90% turn off 90%	$t_{d\ DES(90)}$			2	3	µs
reset to fault	$t_{d\ RES_FAULT}$		3	7	20	µs
minimum pulse width for RESET	PW_{RES_min}		0.1			µs



INPUT-OUTPUT TIMING



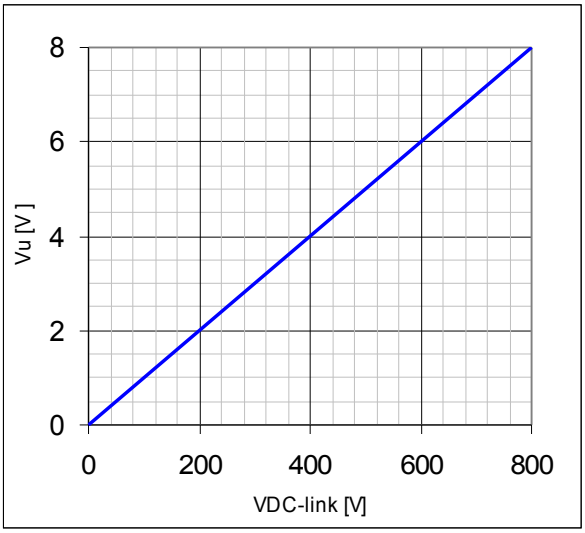
DESATURATION EVENT TIMING

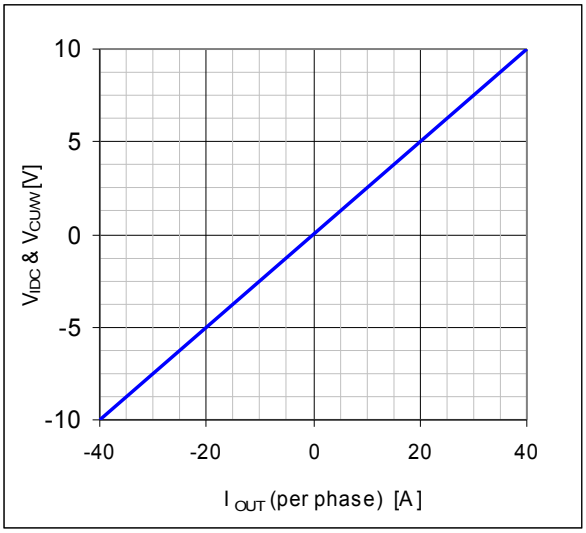
The driver system doesn't generate dead time. The user must ensure a correct dead time generation (no less than 1µs) with the control signals between the two IGBTs from each branch. If both IGBTs from a branch switch on at the same time the module can be damaged.

Reserves the right to change limits, test conditions and dimensions given in this data sheet at any time without previous notice.

SENSORS ELECTRICAL CHARACTERISTICS

Description	symbol	notes/test conditions	Min	Typ	Max	Units
Supply voltage	V_{CC}			±15		V
Feedback signal of output current	$V_{CU/W}$	Accuracy=±0.65%	-10		10	V
Output current measurable range		@I_{meas}=30A	-40		40	A
Feedback signal of VDC-link voltage	V_U	Accuracy=±0.8%	0		8	V
DC-link voltage measurable range		@V_{DC}=800V	0		800	V_{DC}
NTC rated resistance	$R_{NTC\ 25}$	internal module NTC TC=25ºC		5		kΩ
NTC B value	$B_{25/50}$	**Note 1**		3375		K





Note 1:

$$R_T = R_{25} \cdot e^{B\left(\frac{1}{T[K]} - \frac{1}{298{,}15K}\right)}$$

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EXTERNAL CONNECTIONS

Signal connections:

J1 CONTROL CONNECTOR (male DB-25) is the main connector for the control switching signals for each IGBT, reset signal, capacitor precharge relay signal, and output fault signals. Also can be used to supply the drivers +12 V_{DC}. Pinout of this connector below.

J1 - DB25 CONTROL CONNECTOR

DB25 CTL	*designation*	*function*
1	FAULT T1	Fault open collector output signal from T1 channel
2	RESET	Input logic signal for reset the driver
3	PWM T1	Input logic signal for switching T1 IGBT
4	GND CTL	Ground terminal for supply and logic signals
5	NC	Leave this pin unconnected
6	VIN	+12 V_{DC} from supply voltage
7	FAULT T7	Fault open collector output signal from T7 channel
8	GND CTL	Ground terminal for supply and logic signals **(Note 2)**
9	PWM T7	Input logic signal for switching T7 IGBT
10	FAULT T6	Fault open collector output signal from T6 channel
11	GND CTL	Ground terminal for supply and logic signals
12	PWM T6	Input logic signal for switching T6 IGBT
13	RELAY	Input logic signal for switching the DC-Link relay **(Note 3)**
14	PWM T3	Input logic signal for switching T3 IGBT
15	VIN	+12 V_{DC} from supply voltage
16	FAULT T3	Fault open collector output signal from T3 channel
17	PWM T5	Input logic signal for switching T5 IGBT
18	GND CTL	Ground terminal for supply and logic signals
19	FAULT T5	Fault open collector output signal from T5 channel
20	PWM T2	Input logic signal for switching T2 IGBT
21	VIN	+12 V_{DC} from supply voltage
22	FAULT T2	Fault open collector output signal from T2 channel
23	PWM T4	Input logic signal for switching T4 IGBT
24	VIN	+12 V_{DC} from supply voltage
25	FAULT T4	Fault open collector output signal from T4 channel

Note 2: *All ground terminals GND CTL are internally interconnected.*

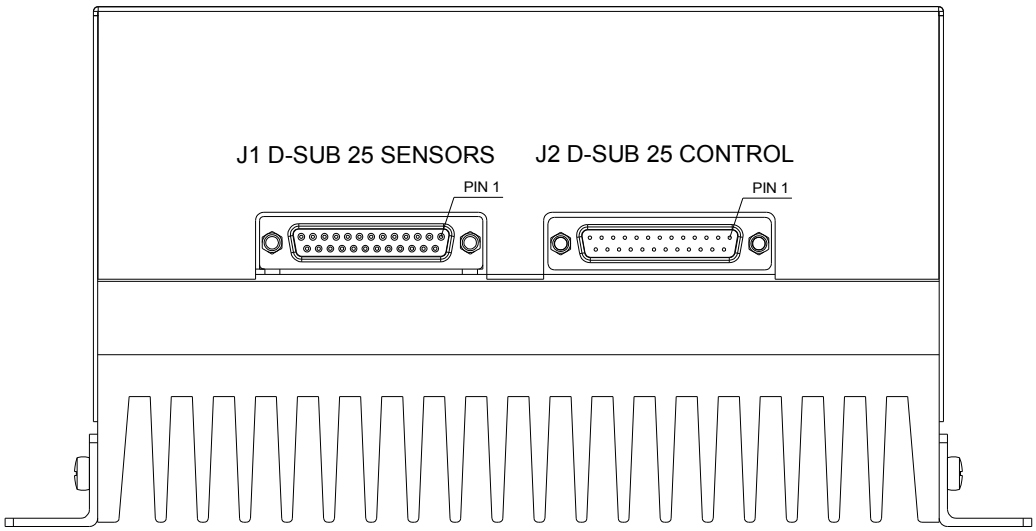
Note 3: *When powered, user must left RELAY signal to 0V (relay open) to precharge the capacitor bank. The user must ensure the capacitor bank is completely charged before switching on the relay (RELAY = 1). This can be accomplished sensing the voltage on DC-Link or wait for 2 seconds.*

Reserves the right to change limits, test conditions and dimensions given in this data sheet at any time without previous notice.

J2 SENSORS CONNECTOR (DB-25 female) is the main connector for the sensor signals: current outputs, DC-Link voltage and temperature sensor from CBI module. Below you can find the pinout for this connector.

J2 - DB25 SENSORS CONNECTOR

DB25 SENS	*designation*	*function*
1	DCLINK U	Leave this pin unconnected
2	DCLINK A	Output signal from DC-Link voltage sensor
3	OUT_I_U	Output signal from U line current sensor
4	OUT_I_V	Output signal from V line current sensor
5	OUT_I_W	Output signal from W line current sensor
6	NC	Leave this pin unconnected
7	NC	Leave this pin unconnected
8	NC	Leave this pin unconnected
9	NC	Leave this pin unconnected
10	NC	Leave this pin unconnected
11	NC	Leave this pin unconnected
12	NC	Leave this pin unconnected
13	NC	Leave this pin unconnected
14	VDD	15 V_{DC} from supply voltage for sensors
15	VDD	15 V_{DC} from supply voltage for sensors
16	VDD	15 V_{DC} from supply voltage for sensors
17	GND SENS	Ground terminal for sensors supply and logic signals **(Note 4)**
18	GND SENS	Ground terminal for sensors supply and logic signals
19	NTC1	NTC1 signal from CBI module
20	NTC2	NTC2 signal from CBI module
21	GND SENS	Ground terminal for sensors supply and logic signals
22	GND SENS	Ground terminal for sensors supply and logic signals
23	VEE	-15 V_{DC} from supply voltage for sensors
24	VEE	-15 V_{DC} from supply voltage for sensors
25	VEE	-15 V_{DC} from supply voltage for sensors



Note 4: *All ground terminals GND SENS are internally interconnected.*

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Power connections:

J3 POWER TERMINALS is the main power connector for the stack. There are the connections for the AC power supply (R, S, T). The output terminals (U,V, W) for the load, Brake and VDCLINK+ to use a brake resistor, earth connection and, additionally, connections for VDCLINK- and VDCLINK+R (positive output of DCLINK at the rectifier bridge output). Below you can find the pinout.

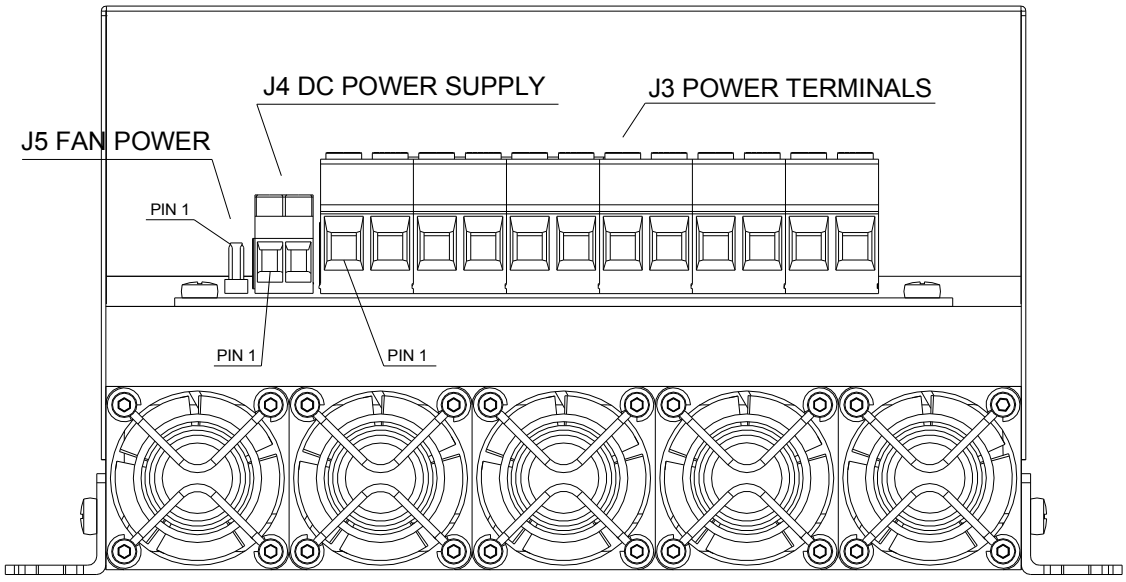
J3 - POWER TERMINALS

PWR CON 1	*designation*	*function*
1	EARTH	Earth connection
2	BRK	Break output
3	NC	Leave this pole unconnected
4	VDCLINK +R	Positive power output from rectifier bridge
5	VDCLINK +I	Positive power output from inverter DC-Link
6	VDCLINK-	Negative power output from inverter DC-Link
7	R IN	R phase input
8	S IN	S phase input
9	T IN	T phase input
10	W OUT	W phase output
11	V OUT	V phase output
12	U OUT	U phase output

J4 DC POWER SUPPLY TERMINALS is the auxiliary power connector for the stack. The system must be supplied with $12V_{DC}$ through this connector. It supplies the power for the drivers, the precharge relay and the fans (if the system has an air forced heatsink).

J4 - DC POWER SUPPLY TERMINALS

DC SUPPLY	*designation*	*function*
1	V_{IN}	+12 V_{DC} supply voltage
2	GND	GND supply voltage



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J5 FAN POWER CONNECTOR is the power connector for the fan system. This connector sinks directly the power from J4 connector.

J5 - FAN POWER CONNECTOR

DC SUPPLY	*designation*	*function*
1	GND	GND supply voltage
2	VIN	+12 V_{DC} supply voltage

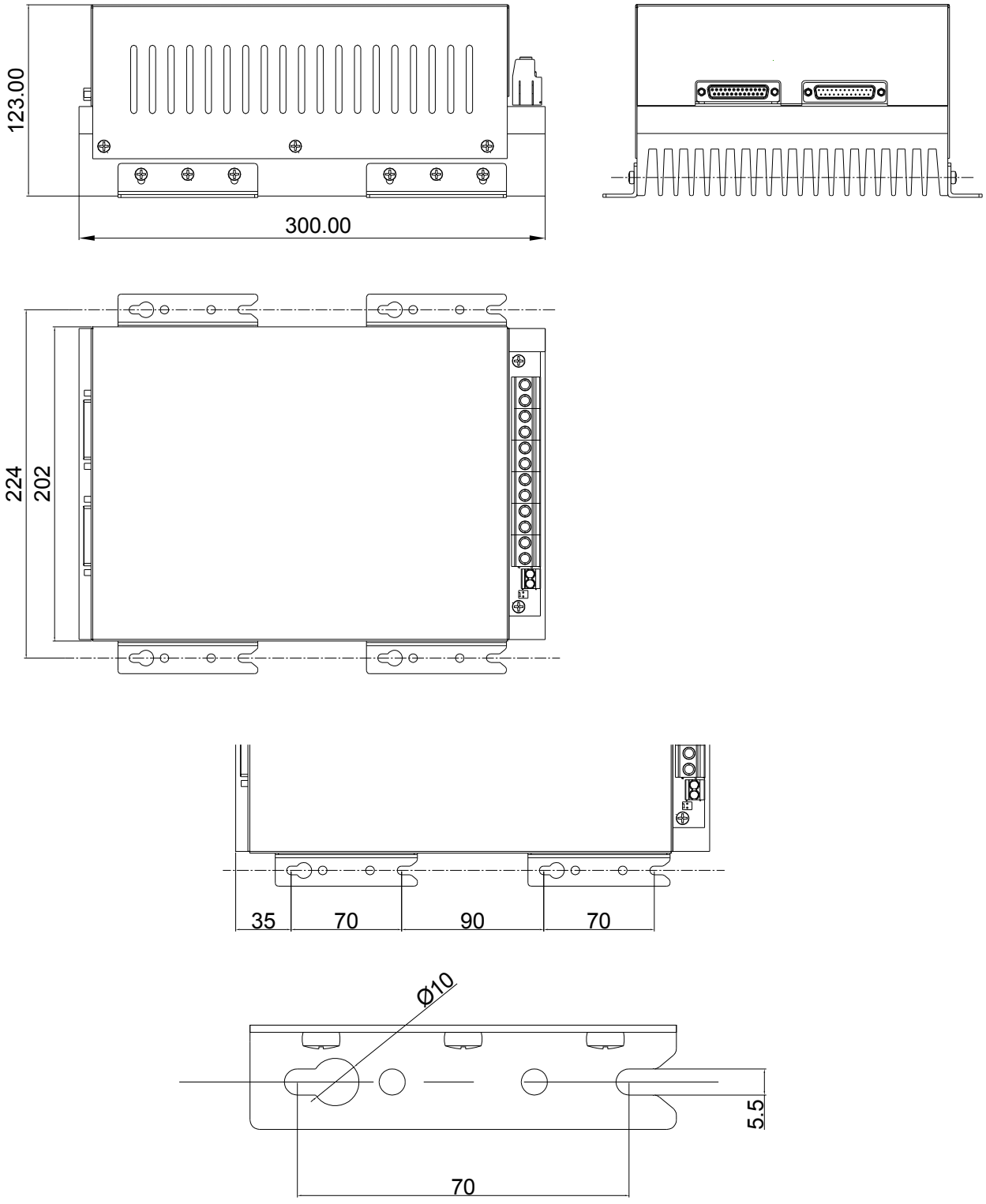
POWER STACK SUPPLIES

Description	*symbol*	*notes/test conditions*	*Min*	*Typ*	*Max*	*Units*
Driver voltage supply	V_{AUX}		10,8	12	13,2	V
Driver power supply	I_{AUX}		300		900	mA
Sensors voltage supply	V_{SENSE}			±15		V
Sensors power supply	I_{SENSE}		50		250	mA

In order to supply the power stack user need to feed the drivers, capacitor bank precharge relay and fan system (if applicable) with 12 V_{DC} from J3 or with the DB25 connector J2 (see: J2-DB25 CONTROL CONNECTOR pinout table).
Sensors must be supplied (±15V) through J1 (see : J1-DB25 SENSORS CONNECTOR pinout table).

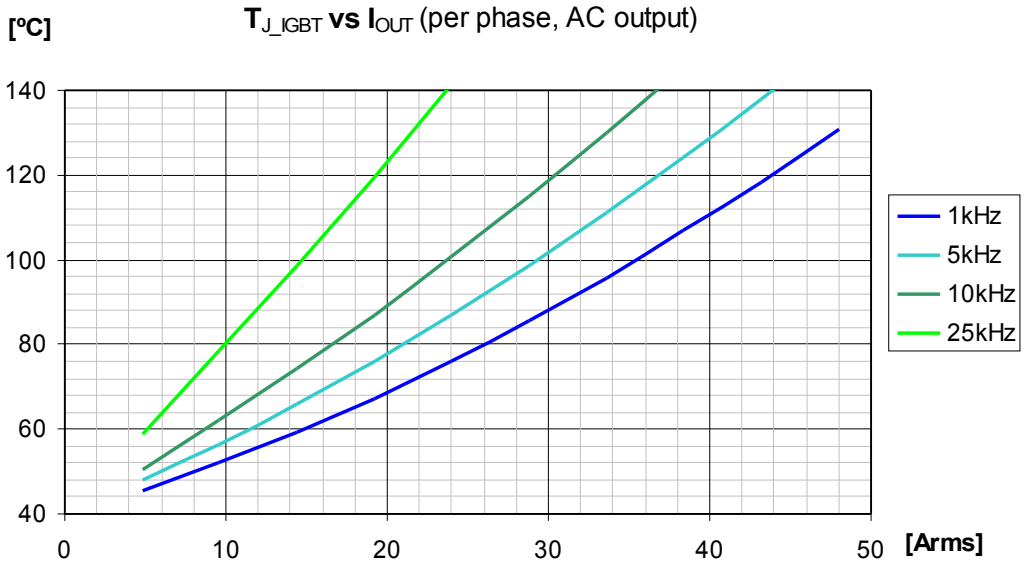
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MECHANICAL DIMENSIONS (All dimensions in mm.)



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PERFORMANCE CURVES



Condition	*symbol*	*value*	*Units*
Ambient temperature	T_A	40	ºC
Input voltage (each phase)	V_{IN}	400	V_{RMS}
Input frequency	f_{in}	50	Hz
DC link voltage	V_{DClink}	555	V_{DC}
Modulation index	m	1	
Load power factor	PF	0,85	
Output frequency	f_{OUT}	50	Hz

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